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09/781,360	02/12/2001	Frank Dumont	PA000005	1724

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EXAMINER

VENT, JAMIE J

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PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

DETAILED ACTION

Response to Arguments

1. Applicant's arguments filed April 21, 2008 have been fully considered but they are not persuasive. On pages 2-6 applicant argues that Kita et al in view of Kim et al fails to disclose a "second circuit at least connectable to the first output, for digitizing the first baseband analog video signal and for processing and outputting a corresponding digital stream on a second output). Kita et al discloses the first and second circuit providing an analog signal on the basis of a digital stream. Additionally, Kita et al discloses the ability to store in memory a digital signal (Figure 8) and thereby meets the limitation of a "second circuit at least connectable to the first output for digitizing a first baseband analog signal and for processing"; however, fails to disclose the output of the digital signal. It is additionally taught by Kim et al to provide various connectable circuits to provide digitalization of a stream prior to output as seen in Figure 1. The digital stream and analog streams can simultaneously outputted through ports 11 and 12 as described in Colum 5 Lines 1-45. Furthermore, the examiner notes that the input signal into Kim provides the ability for analog and digital signals to be processed and altered through the various circuitry to allow for the conversion of a signal to analog or digital. Therefore, information from the memory store of Kita can be digitally outputted through the output port as taught by Kita et al, to allow for the output of a digital signal into the system for processing and displaying. Although, all of applicants points are understood the examiner can not agree.